

Team Name: sdmay24-40

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Report Period: Oct 8 to Oct 22

### **Summary of Progress in this Period**

We have decided the amplifier to use for our ADC board. We have finished the lens modeling. We have finished reviewing and ordering the RF board. We started on the ADC board.

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### **Pending Issues**

We need to work on the ADC schematic and layout.

We should start on programming the FPGA to interface with the computer.

We should start on the back-ends of the GUI

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### **Plans for Upcoming Reporting Period**

We need to finish the ADC schematic and layout.

We start on programming the FPGA to interface with the computer.

We start on back-ends of the GUI.

After the RF board is received, we should test the board.

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